

# 2016 International Workshop for Ultra Low Power Nano-electronics for IoT

**October 18-19, 2016**

International Conference room, 6<sup>th</sup> Floor, HIT Bldg., Hanyang University, Seoul, Korea

## Scientific Program

### Memory devices, analog design and technologies(October 18<sup>th</sup>)

**Capacitor-less memory: advances and challenges**

Prof. Francesco Gamiz (*Univ. of Granada, Spain*)

**Technology options for high energy efficiency with FDSOI**

Dr. Fred Allibert (*SOITEC, France*)

**Low-power analog design fundamentals**

Prof. Bram Nauta (*Univ. of Twente, Netherlands*)

**Steep-slope transistor concepts for low-power CMOS**

Prof. Jurriaan Schmitz (*Univ. of Twente, Netherlands*)

**Sharp switching devices based on band modulation**

Prof. Sorin Cristoloveanu (*Grenoble INP Minatec, France*)

**Technology Challenges and Opportunities of Mobile Memory**

Dr. Seok-Hee Lee (*SK Hynix, Korea*)

### Off the beaten CMOS path(October 19<sup>th</sup>)

**Non-Si MOSFET and TFET for low-power circuits**

Prof. Shinichi Takagi (*Univ. of Tokyo, Japan*)

**Synaptic devices and neuron circuits for neuromorphic chips**

Prof. Byung-Gook Park (*Seoul National Univ., Korea*)

**Taking Gallium Nitride HEMTs into the THz**

Prof. Debdeep Jena (*Cornell Univ., USA*)

**Charge Trapping and Time-dependent Variability in Low-Voltage MOS Transistors**

Prof. Tibor Grasser (*Technical Univ. of Vienna, Austria*)

**Photonics on CMOS is Key Enabling Technology for innovation**

Dr. Maryse Fournier (*LETI, France*)

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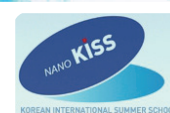


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