About Synopsys

Smart, Secure Everything—From Silicon to Software

Synopsys technology is at the heart of innovations that are changing the way we live and work. The Internet of Things. Autonomous cars. Wearables. Smart medical devices. Secure financial services. Machine learning and computer vision. These breakthroughs are ushering in the era of Smart, Secure Everything—where devices are getting smarter, everything's connected, and everything must be secure.

Powering this new era of technology are advanced silicon chips, which are made even smarter by the remarkable software that drives them. Synopsys is at the forefront of Smart, Secure Everything with the world's most advanced tools for silicon chip design, verification, IP integration, and application security testing. Our technology helps customers innovate from Silicon to Software, so they can deliver Smart, Secure Everything.

Since 1986, Synopsys has been at the heart of accelerating electronics innovation with engineers around the world having used Synopsys technology to successfully design and create billions of chips and systems that are found in the electronics that people rely on every day.

Now Synopsys is currently looking for "R&D Engineer" who will be working in Korea office!

Job Overview

Topography technology becomes crucial for now and future semiconductor device on the roadmap following Moore's law. For the topography simulation such as deposition and etch processes, Synopsys TCAD group has been developing Sentaurus Topography™, which embeds the complicated physics of transport, reaction and geometric evolution as well as the state-of-the-art mathematics and algorithms to implement such physics. Sentaurus Topography™ is the leading-edge simulator for topography in the semiconductor industry. This job position is to develop not only the source code but also the applications for Sentaurus Topography™.

Responsibilities and Duties

- Develop and implement the models for depo/etch process
- Develop the applications using Sentaurus Topography™ for the state-of-the-art depo/etch technology
- Develop the calibration methodology for physical parameters in the depo/etch models
- Support CAEs to solve the problems that customers report
- Participate in collaborative projects with customers and other partners based on business needs

Qualifications

- MS or PhD in Physics, Electrical Engineering, Materials Science or equivalent (PhD preferred)
- Deep knowledge of the theory such as the plasma etch and chemical vapor deposition
- Proficient understanding and experiences in depo/etch simulation
- Good knowledge of the advanced semiconductor process from front-end to back-end
- Strong software programming skills in C++ and python is required

How to apply? If you are interested in the position, please send your English resume to Sangmi Kim (Sangmi.kim@synopsys.com)

Do you have any questions? please reach out Sangmi Kim via her email, Sangmi.kim@synopsys.com or call via 3404-2722.