

About Synopsys

Smart, Secure Everything—From Silicon to Software

Synopsys technology is at the heart of innovations that are changing the way we live and work. The Internet of Things. Autonomous cars. Wearables. Smart medical devices. Secure financial services. Machine learning and computer vision. These breakthroughs are ushering in the era of Smart, Secure Everything—where devices are getting smarter, everything's connected, and everything must be secure.

Powering this new era of technology are advanced silicon chips, which are made even smarter by the remarkable software that drives them. Synopsys is at the forefront of Smart, Secure Everything with the world's most advanced tools for silicon chip design, verification, IP integration, and application security testing. Our technology helps customers innovate from Silicon to Software, so they can deliver Smart, Secure Everything.

Since 1986, Synopsys has been at the heart of accelerating electronics innovation with engineers around the world having used Synopsys technology to successfully design and create billions of chips and systems that are found in the electronics that people rely on every day.

Now Synopsys is currently looking for “Application Engineer (AE) of PDK group” who will be working in Korea office!

Job description

The Application Engineer (AE) of PDK group within Synopsys Design Group (DG) will be involved in the following activities:

- Focused on iPDK/Laker PDK development and foundry qualification.
- Interface with foundry partners to build and deliver PDKs for advanced process technologies.
- Work with the marketing and sales teams to drive and implement a PDK deployment strategy for rapid market adoption.
- Involves interactions with foundry partners to understand the physical design rules constraints, devices implementation and models for accurate implementation at the layout and schematic level.
- Interact with developers, and understand specific tool requirements of schematic or netlist-driven design to develop foundry technology kits that helps designers realize the full design potential.

Qualifications

- Typically requires no previous professional experience but needs proficiency in computer languages: Tcl and Python.
- (Mandatory) Experience and proficiency in computer languages: Tcl and Python. Perl and Tk extensions for graphical interface creation are a plus.
- (Preferred) Experience and knowledge in Analog/Mixed Signal design and/or PDK.
- Ability to consistently meet deadlines, and work both independently and as part of a team.
- Good communication skills in Korean/English and the ability to multi-task essential for success in this role.
- (Preferred) Knowledge of analog design environment, physical verification and circuit simulation tools is a plus.
- (Preferred) Knowledge of layout considerations in analog/mixed signal designs is a plus.

How to apply? If you are interested in the position, please send your English resume to Sangmi Kim at Sangmi.kim@synopsys.com. If you have any questions, please reach out Sangmi Kim via her email, Sangmi.kim@synopsys.com or call via 3404-2722.